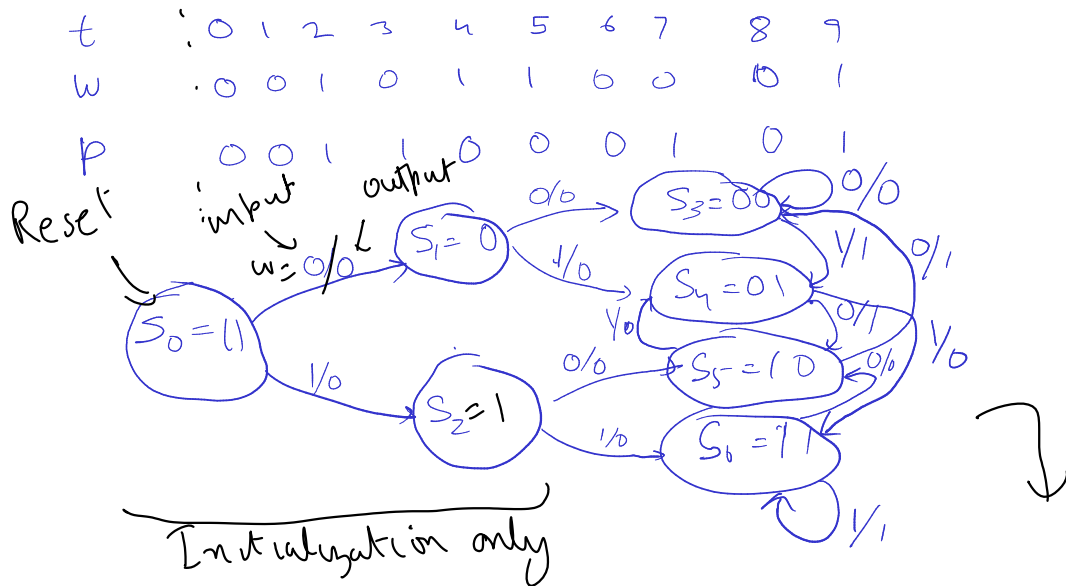


Practice problems

Problem 1

Example sequence (Mealy)



State table

Seq	Present state	Next State		Output (p)	
		w=0	w=1	w=0	w=1
"1"	S ₀	S ₁	S ₂	0	0
"0"	S ₁	S ₃	S ₄	0	0
"1"	S ₂	S ₅	S ₆	0	0
"00"	S ₃	S ₃	S ₄	0	1
"01"	S ₄	S ₅	S ₆	1	0
"10"	S ₅	S ₃	S ₄	1	0
"11"	S ₆	S ₅	S ₆	0	1

Problem 1 Alternative solution

Example sequence

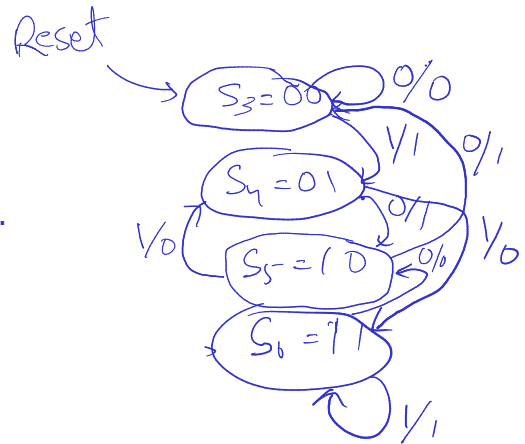
time \rightarrow

Input $w = 00 \text{ ; } 010110001$

Output $p = \text{ ; } 010000101$

Reset \downarrow Assume that inputs before reset were '00'

By making a different assumption, we are able to remove the 3 initialization states.



State table

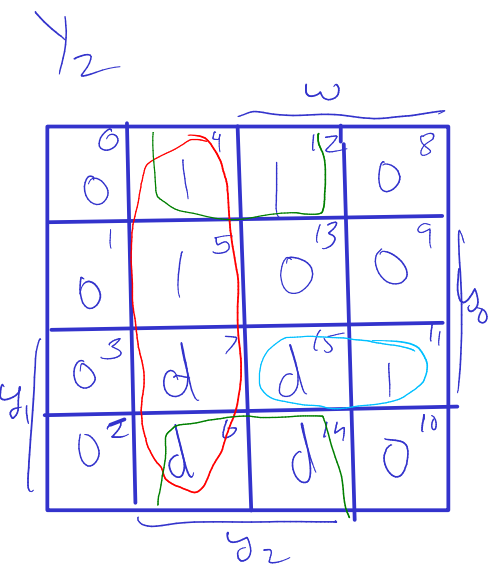
eg	Present state	Next State		Output (p)	
		w=0	w=1	w=0	w=1
"00"	S ₃	S ₃	S ₄	0	1
"01"	S ₄	S ₅	S ₆	1	0
"10"	S ₅	S ₃	S ₄	1	0
"11"	S ₆	S ₅	S ₆	0	1

Problem 2

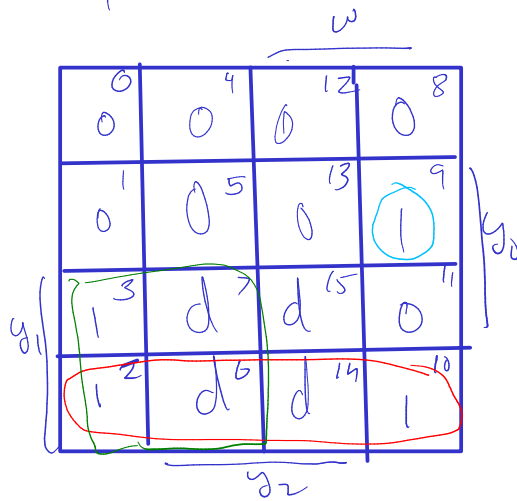
Moore modulo 6 counter

State assigned table (Not optimal)

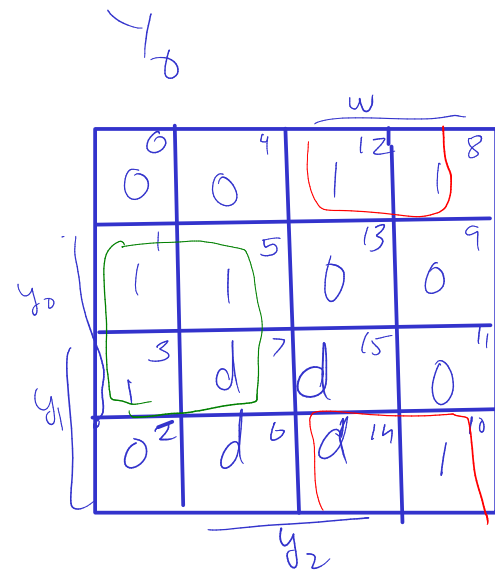
Present State $y_2 y_1 y_0$	Next state		Output $Z_2=y_2, Z_1=y_1, Z_0=y_0$
	$w=0$ $Y_2 Y_1 Y_0$	$w=1$ $Y_2 Y_1 Y_0$	
0 0 0	0 0 0	0 0 1	
0 0 1	0 0 1	0 1 0	
0 1 0	0 1 0	0 1 1	
0 1 1	0 1 1	1 0 0	
1 0 0	1 0 0	1 0 1	
1 0 1	1 0 1	0 0 0	
1 1 0	d d d	d d d	
1 1 1	d d d	d d d	



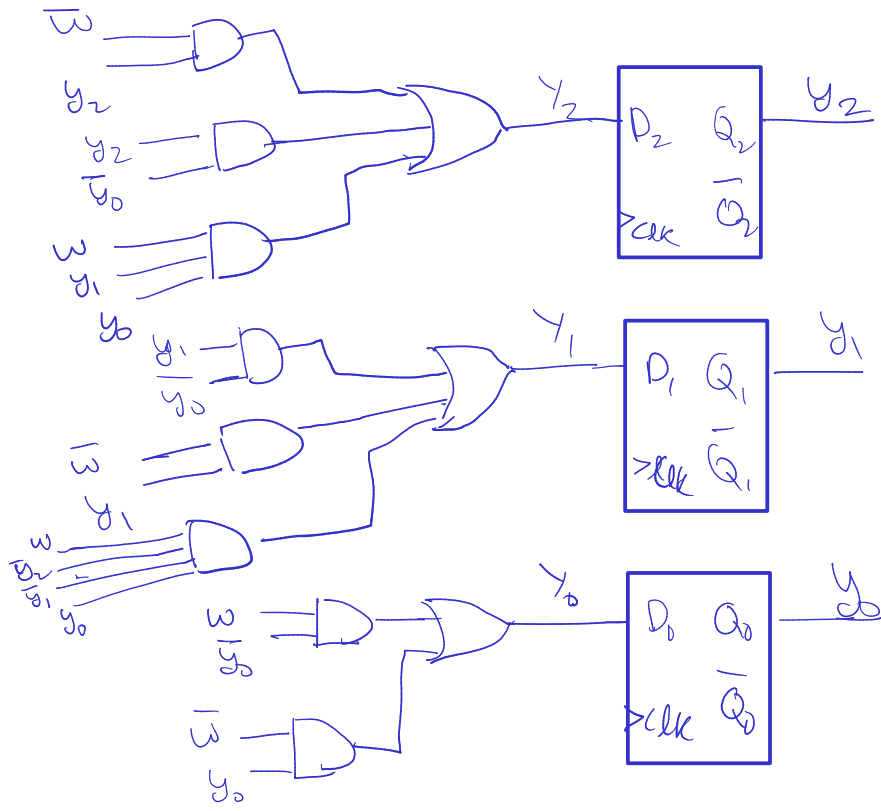
$$Y_2 = \bar{w}y_2 + y_2\bar{y}_0 + wy_1y_0$$



$$Y_1 = y_1\bar{y}_0 + \bar{w}y_1 + wy_2\bar{y}_1y_0$$



$$Y_0 = wy_0 + \bar{w}y_0$$



Problem 3

3-bit counter like circuit (Moore)

State assigned table

Present State $y_2 \ y_1 \ y_0$	Next State			Output $Z_2=y_2 \ Z_1=y_1 \ Z_0=y_0$
	$w=0$ $y_2^+ \ y_1^+ \ y_0^+$	$w=1$ $y_2^+ \ y_1^+ \ y_0^+$		
0 0 0	1 1 1	0 1 0		
0 0 1	0 0 0	0 1 1		
0 1 0	0 0 1	1 0 0		
0 1 1	0 1 0	1 0 1		
1 0 0	0 1 1	1 1 0		
1 0 1	1 0 0	1 1 1		
1 1 0	1 0 1	0 0 0		
1 1 1	1 1 0	0 0 1		

How to convert Y_2+ K-map to J_2 and K_2 K-map?

J-K excitation table

y_2	y_2^+	J_2	K_2	
0	0	0	d	(hold or reset)
0	1	1	d	(toggle or set)
1	0	d	1	(toggle or reset)
1	1	d	0	(hold or set)

y_2	y_2^+	J_2
0	0	0
0	1	1
1	0	d
1	1	d

when $y_2=0$
 $J_2 = y_2^+$
 when $y_2=1$
 then $J_2 = d$

y_2	y_2^+	K_2
0	0	d
0	1	d
1	0	1
1	1	0

when $y_2=0$
 then $K_2 = d$
 when $y_2=1$
 then $K_2 = \overline{y_2^+}$

y_2	J_2
0	y_2^+
1	d

y_2	K_2
0	d
1	$\overline{y_2^+}$

where $y_2 = 1$
 put $J_2 = d$

where $y_2 = 0$
 put $K_2 = d$

where $y_2 = 0$
 put $J_2 = y_2^+$

where $y_2 = 1$
 put $K_2 = \overline{y_2^+}$

y_2^+

w			
1 ⁰	0 ⁴	1 ¹²	0 ⁸
0 ¹	1 ⁵	1 ¹³	0 ⁹
0 ³	1 ⁷	0 ¹⁵	1 ¹¹
0 ²	1 ⁶	0 ¹⁴	1 ¹⁰

y_1

y_2

J_2

w			
1 ⁰	d ⁴	d ¹²	0 ⁸
0 ¹	d ⁵	d ¹³	0 ⁹
0 ³	d ⁷	d ¹⁵	1 ¹¹
0 ²	d ⁶	d ¹⁴	1 ¹⁰

y_1

y_2

K_2

w			
d ⁰	1 ⁴	0 ¹²	d ⁸
d ¹	0 ⁵	0 ¹³	d ⁹
d ³	0 ⁷	1 ¹⁵	d ¹¹
d ²	0 ⁶	1 ¹⁴	d ¹⁰

y_1

y_2

y_2 y_2^+ J_2 K_2
 0 0 0 d
 0 1 1 d
 1 0 d 1
 1 1 d 0

$$J_2 = \overline{w} \overline{y_1} \overline{y_0} + w y_1$$

$$K_2 = \overline{w} \overline{y_1} \overline{y_0} + w y_1$$

y_1^+

w			
1 ⁰	1 ⁴	1 ¹²	1 ⁸
0	0 ⁵	1 ¹³	1 ⁹
1 ³	1 ⁷	0 ¹⁵	0 ¹¹
0 ²	0 ⁶	0 ¹⁴	0 ¹⁰
y_2			

J_1

w			
1 ⁰	1 ⁴	1 ¹²	1 ⁸
0	0 ⁵	1 ¹³	1 ⁹
d ³	d ⁷	d ¹⁵	d ¹¹
d ²	d ⁶	d ¹⁴	d ¹⁰
y_2			

K_1

w			
d ⁰	d ⁴	d ¹²	d ⁸
d	d ⁵	d ¹³	d ⁹
0 ³	0 ⁷	1 ¹⁵	1 ¹¹
1 ²	1 ⁶	1 ¹⁴	1 ¹⁰
y_2			

$\bar{J}_1 = \bar{w} y_0$

$\Rightarrow J_1 = w + \bar{y}_0$

$\bar{K}_1 = \bar{w} y_0$

$\Rightarrow K_1 = w + \bar{y}_0$

y_0^+

w			
1 ⁰	1 ⁴	0	0
0	0 ⁵	1 ¹³	1 ⁹
0 ³	0 ⁷	1 ¹⁵	1 ¹¹
1 ²	1 ⁶	0 ¹⁴	0 ¹⁰
y_2			

J_0

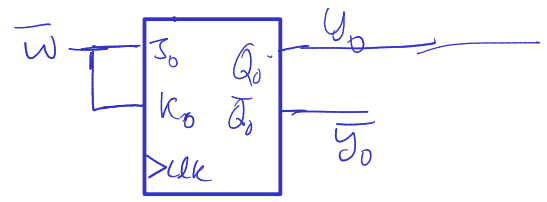
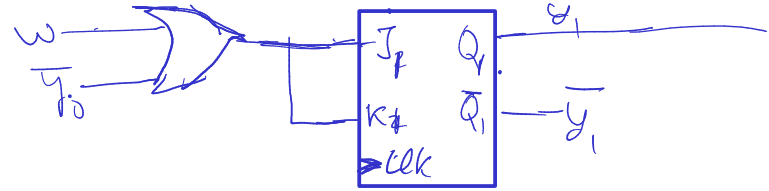
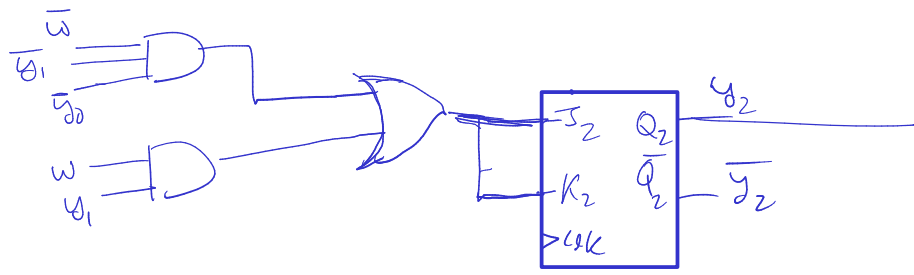
w			
1 ⁰	1 ⁴	0	0
d	d ⁵	d ¹³	d ⁹
d ³	d ⁷	d ¹⁵	d ¹¹
d ²	d ⁶	d ¹⁴	d ¹⁰
y_2			

K_0

w			
d ⁰	d ⁴	d ¹²	d ⁸
1	1 ⁵	0 ¹³	0 ⁹
1 ³	1 ⁷	0 ¹⁵	0 ¹¹
d ²	d ⁶	d ¹⁴	d ¹⁰
y_2			

$J_0 = \bar{w}$

$K_0 = \bar{w}$



Problem 4

- ① Row reduction
- ② Implication table/chart

Two states are equivalent if next states for every input and outputs for every input are equivalent

Implication table/chart

a	///	///	///	///	///	///	///	///	
b	$e=c$	///	///	///	///	///	///	///	
c	X	X	///	///	///	///	///	///	
d	$e=h$	$c=b$	X	///	///	///	///	///	
e	$e=a$	$e=a$	$h=f$	X	///	///	///	///	
f	X	X	$b=e$	$h=g$	$i=e$	X	///	///	
g	$a=h$	$c=h$	X	$a=b$	X	X	///	///	
h	$e=b$	$e=b$	X	X	X	X	///	///	
i	$e=f$	$c=f$	$h=d$	X	$b=g$	$c=e$	X	///	
	$e=b$	$e=b$	$h=f$	$a=b$	X	X	$h=f$	X	
	a	b	c	d	e	f	g	h	i

Present State	Next State		Present Output (Z)
	X=0	X=1	
a	e	e	1
b	c	e	1
c	i	h	0
d	h	a	1
e	i	f	0
f	e	g	0
g	h	b	1
h	c	d	0
i	f	b	1

① X out different states due to output being different

② Fill out the cells with conditions that need to be true for the corresponding states to be equivalent.

③ cross out impossible conditions

Equivalence $\frac{a=b}{a}$, $\frac{c=e}{c}$, $\frac{d=i=g}{d}$, $\frac{f=h}{f}$

Reduced state table	NS		Output (z)
	X=0	X=1	
a	c	c	1
c	d	f	0
d	f	g	1
f	c	d	0

Problem 5.1

X = Due to mismatching output

X = Due to $S_3 \neq *$
X = Due to $S_6 \neq *$

S_0	S_1	S_2	S_3	S_4	S_5	S_6		Present State	Next State		Output	
									X=0	1	0	1
S_0	$S_5=S_6$							S_0	S_5	S_1	0	0
S_1	$S_2=S_5$	$S_2=S_5$						S_1	S_5	S_6	0	0
S_2								S_2	S_2	S_6	0	0
S_3	X	X	X					S_3	S_0	S_1	1	0
S_4	$S_4=S_5$ $S_3=S_5$	$S_4=S_5$ $S_3=S_6$	$S_5=S_6$	X				S_4	S_4	S_3	0	0
S_5	✓	$S_5=S_5$ $S_1=S_6$	$S_5=S_2$ $S_1=S_6$	X	$S_5=S_4$ $S_1=S_3$			S_5	S_0	S_1	0	0
S_6	X	X	X	$S_6=S_5$	X	X		S_6	S_5	S_1	1	0
	S_0	S_1	S_2	S_3	S_4	S_5	S_6					

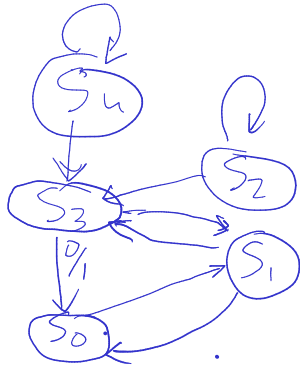
Replace S_5 with S_0 , S_6 with S_3 . State reduction of Binary state table still leaves us with 5 states instead of 7.

FL Tfflop's state table has only 3 states.

Mr Tfflop is not correct.

PS	NS		Output	
	X=0	1	X=0	X=1
S_0	S_0	S_1	0	0
S_1	S_0	S_3	0	0
S_2	S_2	S_3	0	0
S_3	S_0	S_1	1	0
S_4	S_4	S_3	0	0

Problem 5.2



If S_0 is the start state S_2, S_4 are unreachable.
 Remove S_2, S_4 from the state table

Combining with $S_5 \equiv S_0, S_3 \equiv S_6$ we get:

PS	NS		Output	
	X=0	1	X=0	1
S_0	S_0	S_1	0	0
S_1	S_0	S_3	0	0
S_3	S_0	S_1	1	0

	Next State		Output	
	X=0	1	0	1
a	a	b	0	0
b	a	c	0	0
c	a	b	1	0

Comparing with Jpflop's table

$S_3 \equiv c$ because output (1,0) is unique

$S_1 \equiv b$

$S_0 \equiv a$

In this case Mr Jpflop is correct

Problem 6

X = Due to mismatch in output

A									
B	X								
C	A=F B=E C=G	X							
D	X	E=A	X						
E	A=I B=G	X	F=I	X					
F	A=H B=I	X	F=I G=I	X	I=H G=I				
G	X	E=A	X	A=F	X	X			
H	A=E G=B	X	G=B	X	I=F G=B	I=B	X		
I	X	A=I	X	A=I	X	X	E=F	X	
	A	B	C	D	E	F	G	H	I

	X=0	1	Z
A	A	B	1
B	C	E	0
C	F	G	1
D	C	A	0
E	I	G	1
F	H	I	1
G	C	F	0
H	F	B	1
I	C	E	0

A = H = F B = I D = G

Replace H and F with A
I with B
G with D

PS	NS		Z
	X=0	1	
A	A	B	1
B	C	E	0
C	A	D	1
D	C	A	0
E	B	D	1

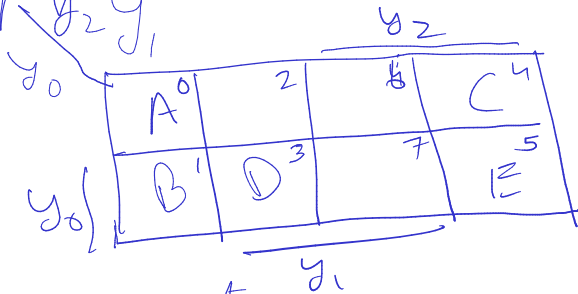
Problem

6.2

Highest Priority: $(A, \checkmark C)$, $(B, \checkmark D)$, $(\checkmark C, \checkmark E)$, $(\checkmark A, \checkmark D)$

Medium Priority: $(A, \checkmark B)$, $(\checkmark C, \checkmark E)$, $(\checkmark A, \checkmark D)$, $(\checkmark C, \checkmark A)$, $(\checkmark B, \checkmark D)$

State Map



Assignment

y_2	y_1	y_0	State
0	0	0	A
0	0	1	B
0	1	0	D
0	1	1	E
1	0	0	C
1	0	1	F
1	1	0	G
1	1	1	H

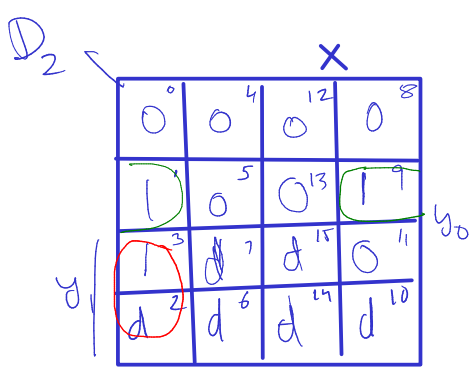
	X=0	1	Z
A	A	B	1
B	C	E	0
C	F	G	1
D	C	A	0
E	I	G	1
F	H	I	1
G	C	F	0
H	F	B	1
I	C	E	0

PS

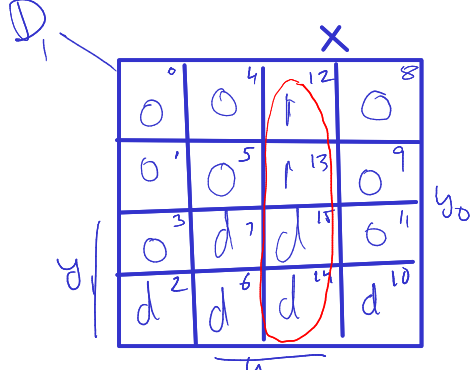
NS

Output (Z)

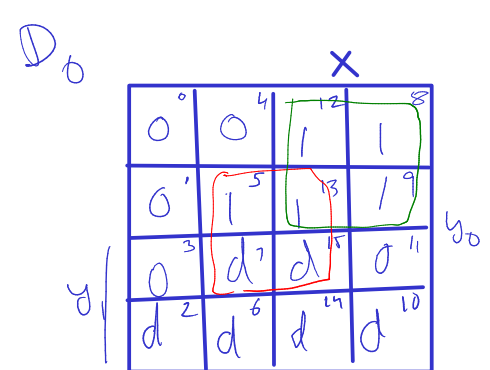
	PS			NS			Output (Z)
	y_2	y_1	y_0	X=0 y_2, y_1, y_0	X=1 y_2, y_1, y_0		
A \equiv H \equiv F	0	0	0	0 0 0	0 0 1	1	
B \equiv I	0	0	1	d d d	d d d	0	
D \equiv G	0	1	0	d d d	d d d	0	
C	1	0	0	0 0 0	0 0 1	0	
E	1	0	1	d d d	d d d	0	
F	1	1	0	d d d	d d d	0	
G	1	1	1	d d d	d d d	0	



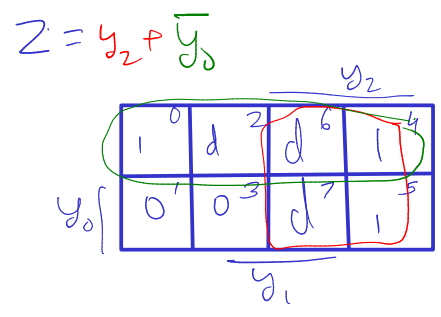
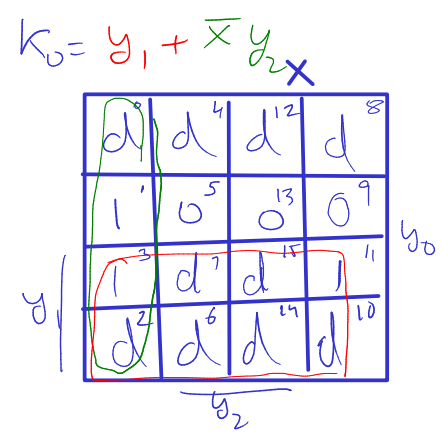
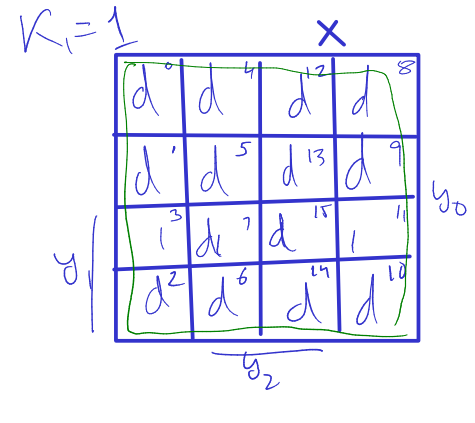
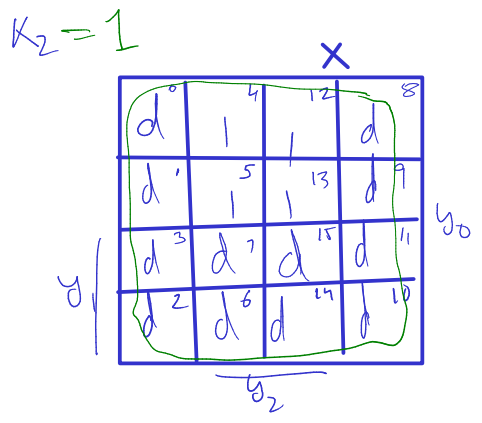
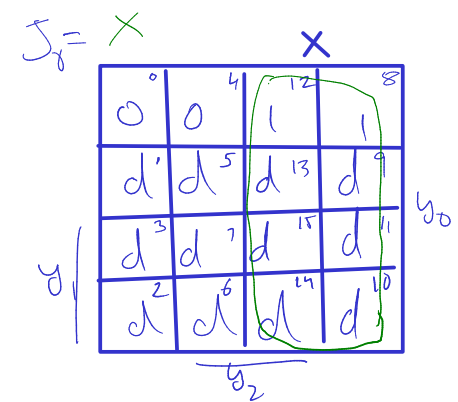
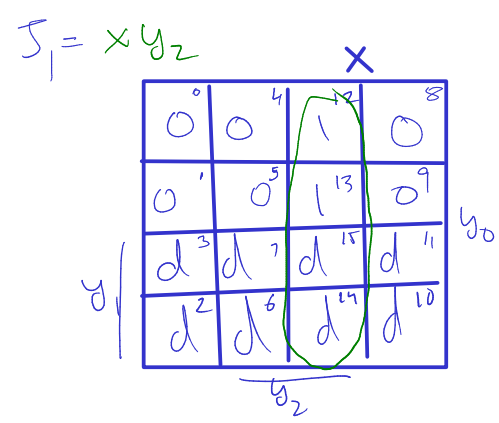
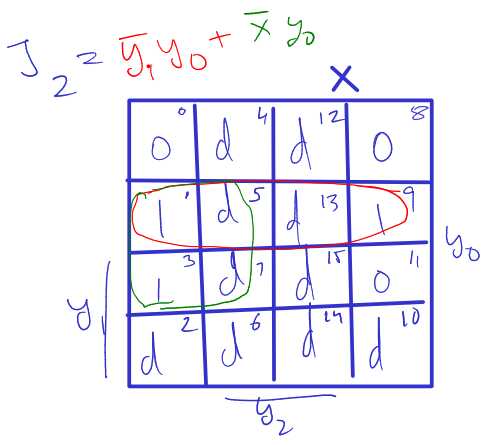
$$D_2 = \bar{y}_2 \bar{y}_1 y_0 + \bar{x} \bar{y}_2 y_1$$



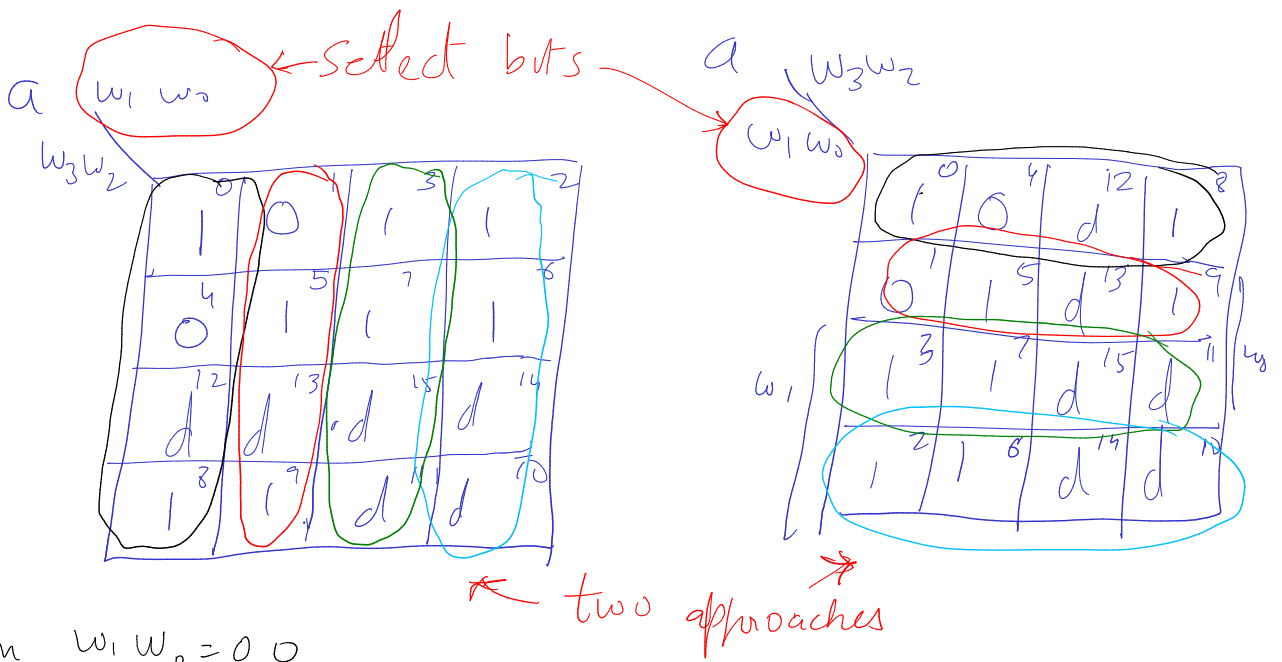
$$D_1 = x y_2$$



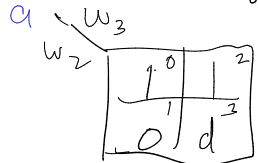
$$D_0 = x \bar{y}_1 + y_2 y_0$$



Prob 7
Part 2

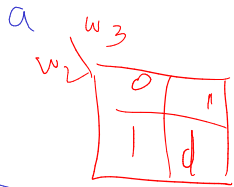


when $w_1 w_0 = 00$



$$a = D_0 = \overline{w_2}$$

when $w_1 w_0 = 01$



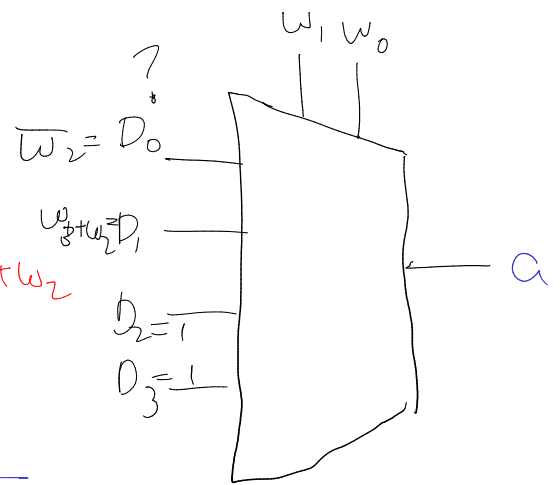
$$a = D_1 = w_3 + w_2$$

when $w_1 w_0 = 10$

$$a = D_2 = 1$$

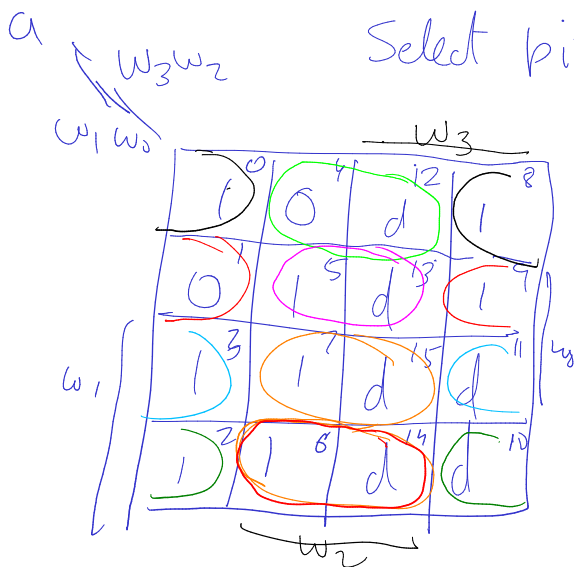
when $w_1 w_0 = 11$

$$a = D_3 = 1$$

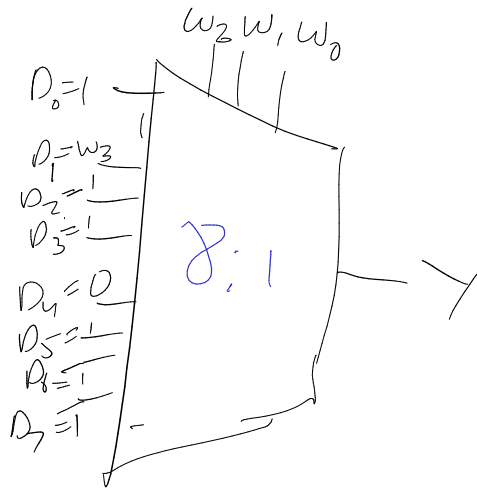


Prob 7
Part 1

Select bits $w_2 w_1 w_0$

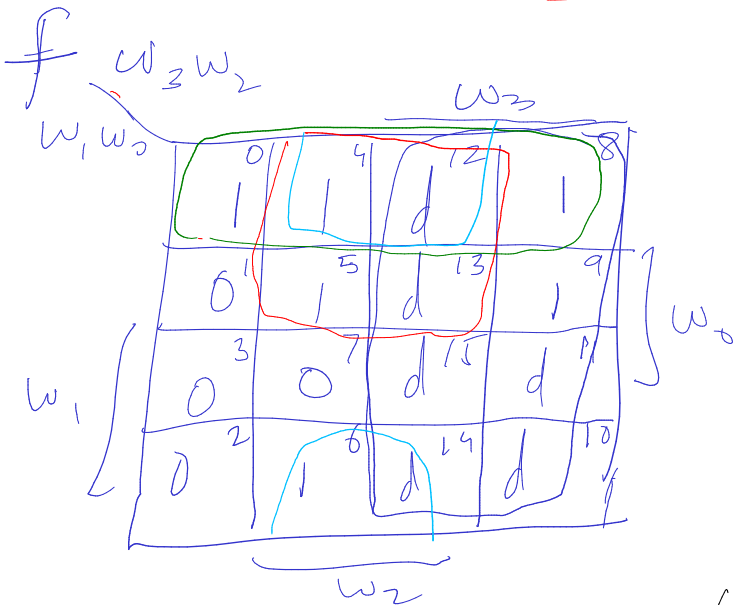


when	w_2	w_1	w_0	
	0	0	0	$D_0 = 1$
	0	0	1	$D_1 = w_3$
	0	1	0	$D_2 = 1$
	0	1	1	$D_3 = 1$
	1	0	0	$D_4 = 0$
	1	0	1	$D_5 = 1$
	1	1	0	$D_6 = 1$
	1	1	1	$D_7 = 1$



Problem 7, Part 3

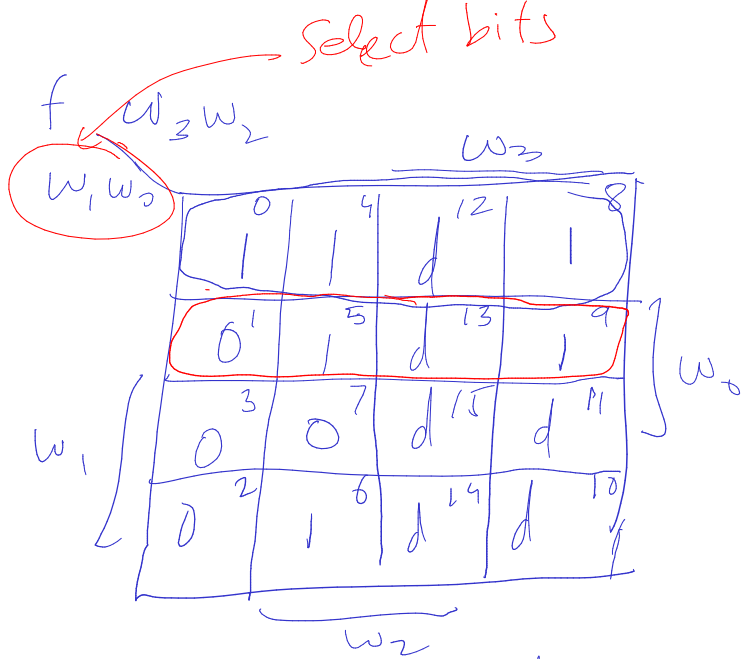
Row	w_3	w_2	w_1	w_0	a	b	c	d	e	f	g
0	0	0	0	0	1	1	1	1	1	1	0
1	0	0	0	1	0	1	1	0	0	0	0
2	0	0	1	0	1	1	0	1	1	0	1
3	0	0	1	1	1	1	1	1	0	0	1
4	0	1	0	0	0	1	1	0	0	1	1
5	0	1	0	1	1	0	1	1	0	1	1
6	0	1	1	0	1	0	1	1	1	1	1
7	0	1	1	1	1	1	1	0	0	0	0
8	1	0	0	0	1	1	1	1	1	1	1
9	1	0	0	1	1	1	1	1	0	1	1



$$f = w_3 + \overline{w_1} w_2 + \overline{w_0} \overline{w_1} + \overline{w_0} w_2$$

Pick w_1, w_0 as select bits

- w_3 occurs 1 times in f
- w_2 " 2 " " f
- w_1 " 2 " " f
- w_0 " 2 " " f

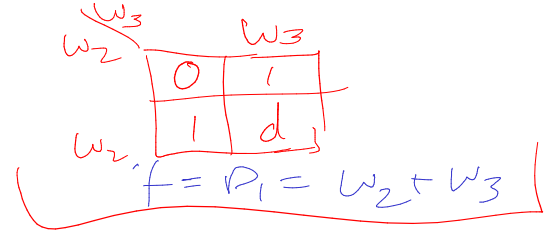


when $w_1, w_0 = 00$

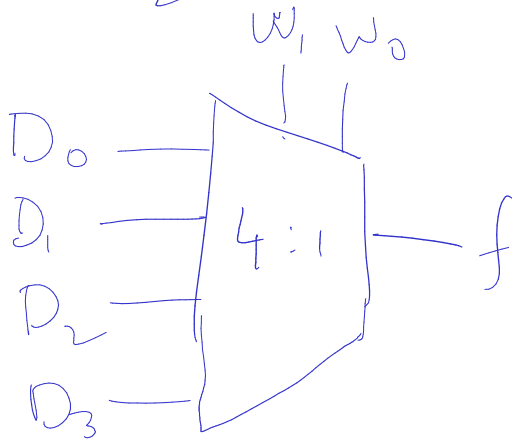
$$f = D_0 = 1$$

when $w_1, w_0 = 01$

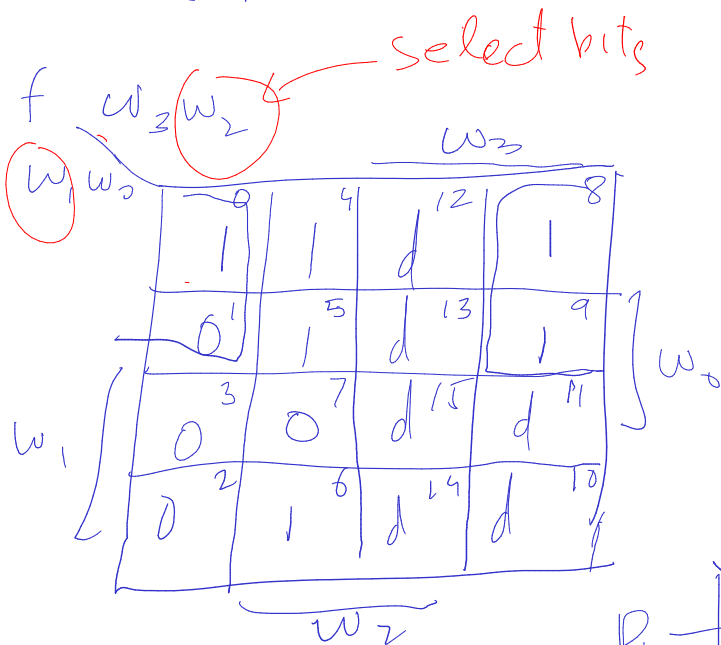
$$f = D_1 = ?$$



FAIL: Problem says cannot use another gate

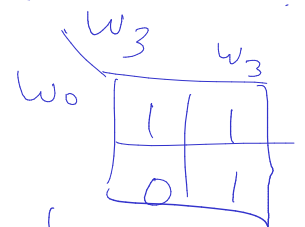


Pick w_2, w_1 as the select bits

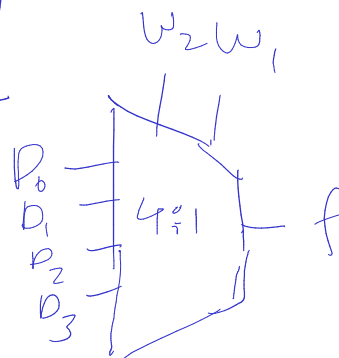


when $w_2, w_1 = 00$

$$f = D_0 = ?$$

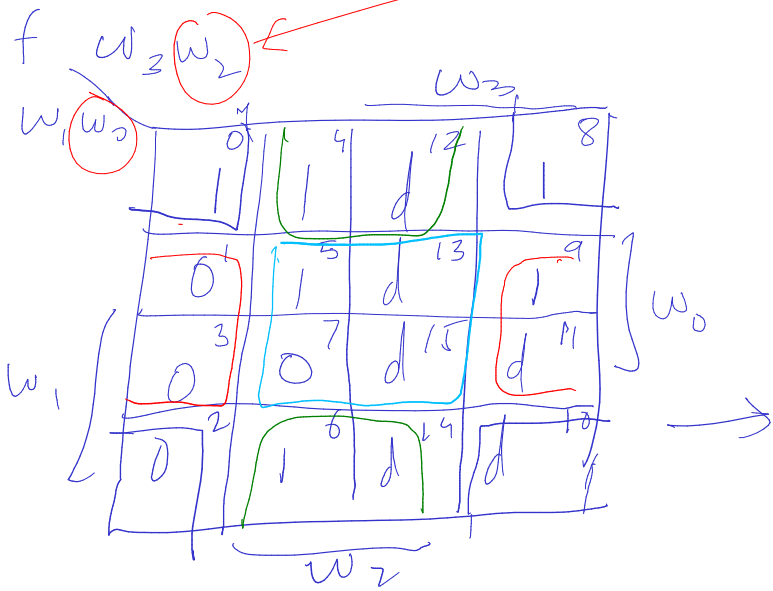


$$f = D_0 = \bar{w}_0 + w_3$$



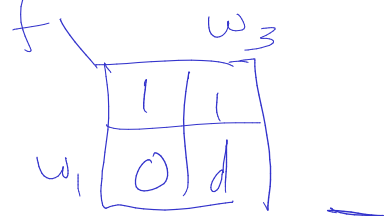
FAIL problem says cannot use another gate

Pick $w_2 w_0$ as the select bits



When $w_2 w_0 = 00$

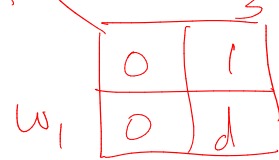
$f = D_0 = ?$



$f = D_0 = \overline{w_1}$

when $w_2 w_0 = 01$

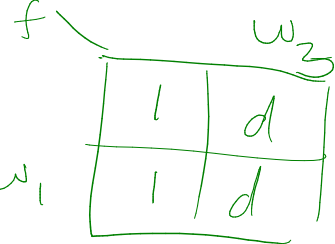
$f = D_1 = ?$



$f = D_1 = w_3$

when $w_2 w_0 = 10$

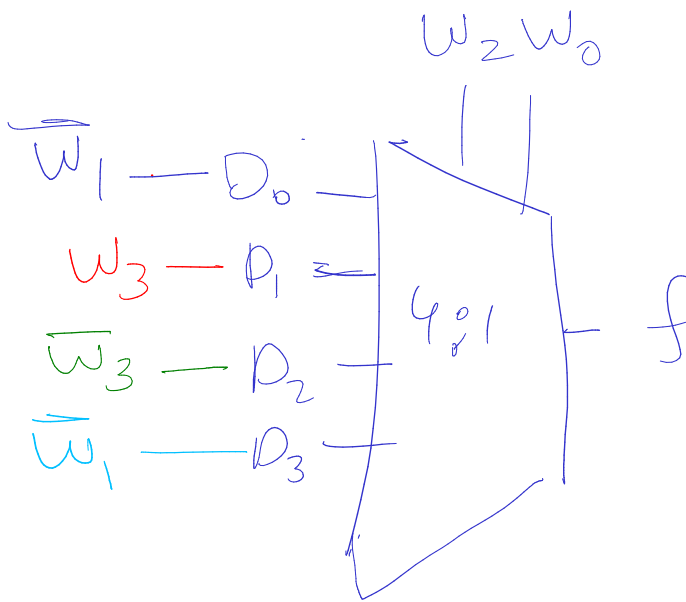
$f = D_2 = ?$



$f = D_2 = \overline{w_3}$

When $w_2 w_0 = 11$

$f = D_3 = ? = \overline{w_1}$



SUCCESS No other gates needed

Problem 8

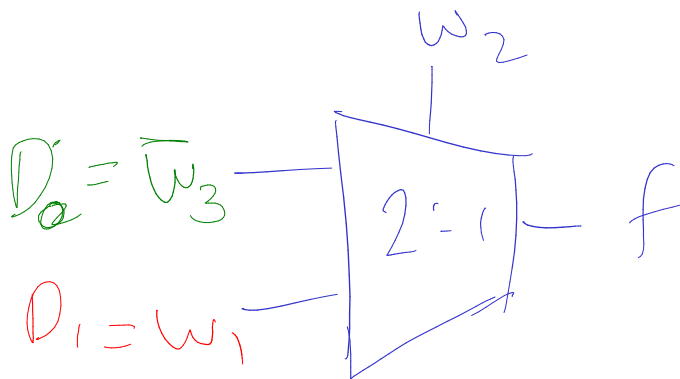
$$f = \overline{w_2} \overline{w_3} + w_1 w_2$$

w_2 occurs 2 times

w_3 " " " "

w_1 " " " "

Pick w_2 as the select bit



	w_1	w_3	
w_2	0	0	0
	1	1	0

Labels: f , $w_3 w_2$, w_1 , w_2 , w_3

When $w_2 = 0$

$$f = D_0 = ?$$

	w_3
w_1	1 0
	1 0

$$f = \overline{w_3} = D_0$$

When $w_2 = 1$

$$f = D_1 = ?$$

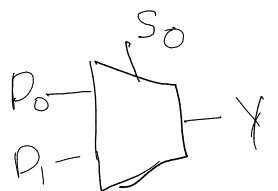
	w_3
w_1	0 0
	1 1

$$f = w_1 = D_1$$

Method 2

2:1 MUX equation

$$Y = \overline{S_0} D_0 + S_0 D_1$$



compare with $f = \overline{w_2} \overline{w_3} + w_1 w_2$

$$f = \overline{w_2} \overline{w_3} + w_2 w_1$$
$$Y = \overline{S_0} D_0 + S_0 D_1$$

Problem 9

$$f(w_1, w_2, w_3) = \sum m(0, 2, 3, 4, 5, 7)$$

